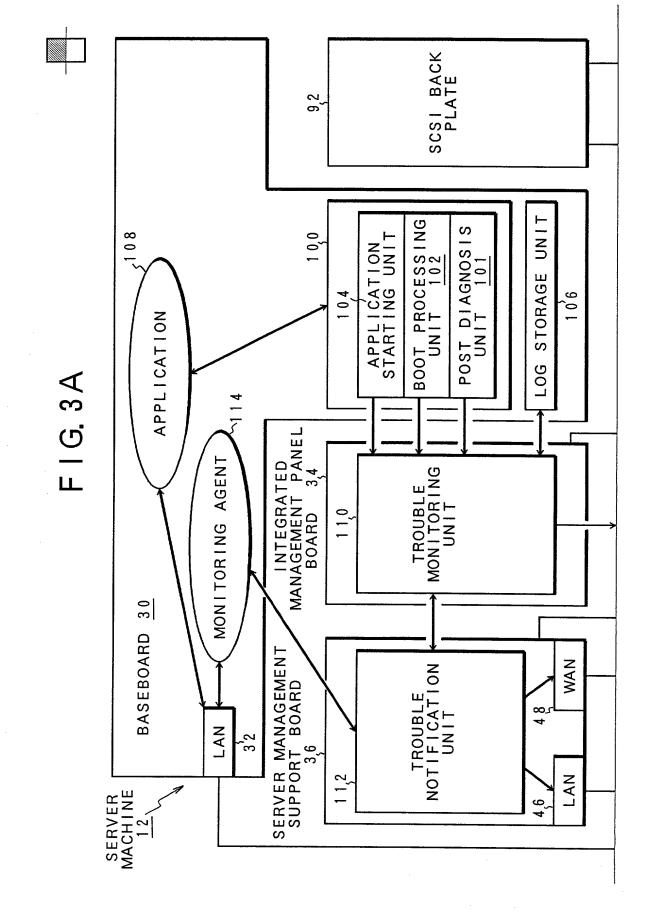
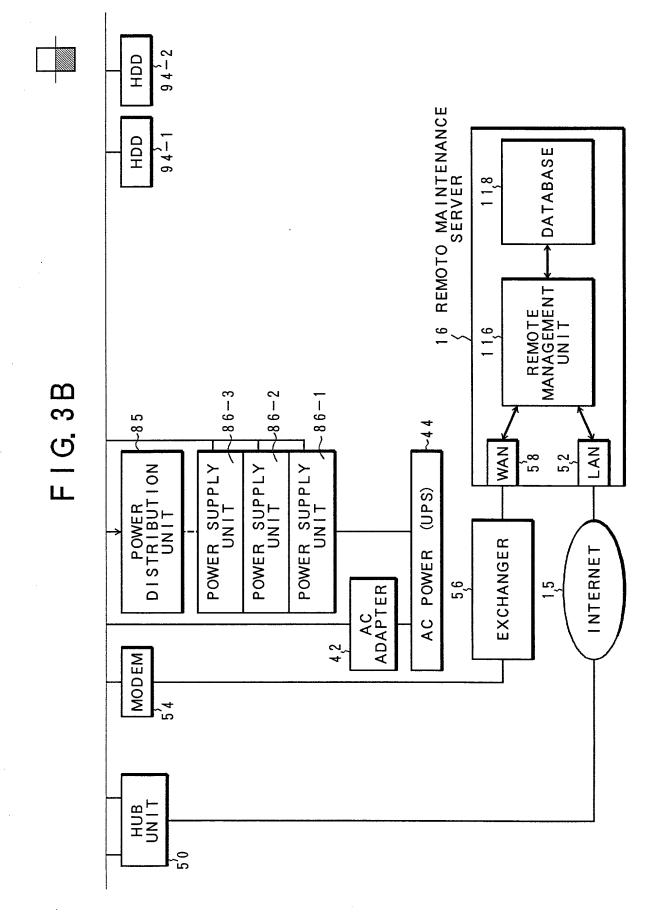


BASEBOARD 12-1 PCI BUS 6,6 NONVOLATILE MEMORY 9,2 8['] 84 - 1FAN 64 - 3CPU F I G. 2A IMP CONTROLLER INTEGRATED MANAGEMENT PANEL BOARD O & 68√MAIN MEMORY 12C 82-1 3,4 64-2CPU 2-212C ∞ SERVER MANAGEMENT SUPPORT BOARD 64-1 CPU WAN INTERFACE CONTROLLER **4** ∞′ ~32 3,6 LAN ω « 0 6 LAN

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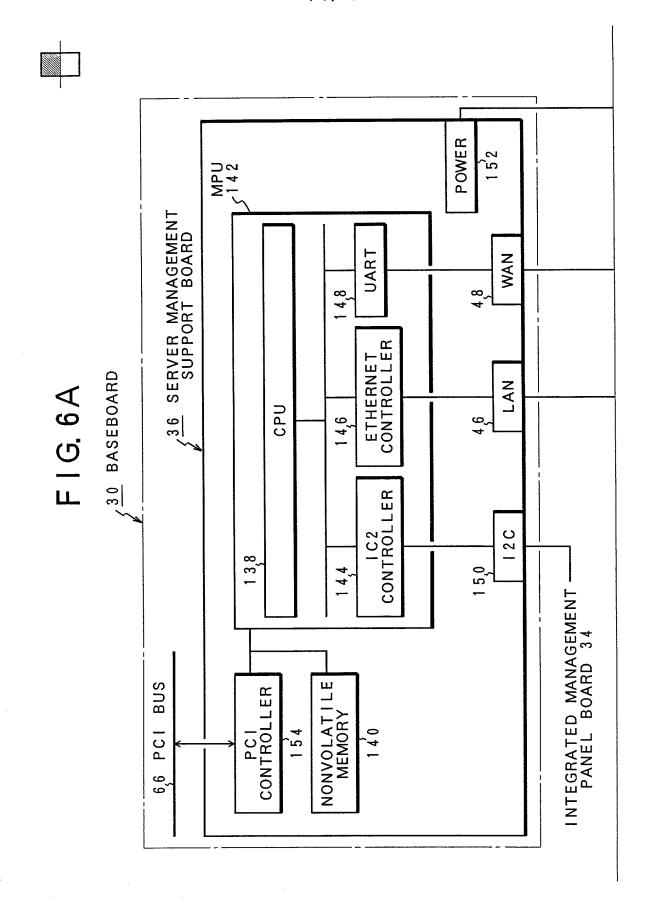
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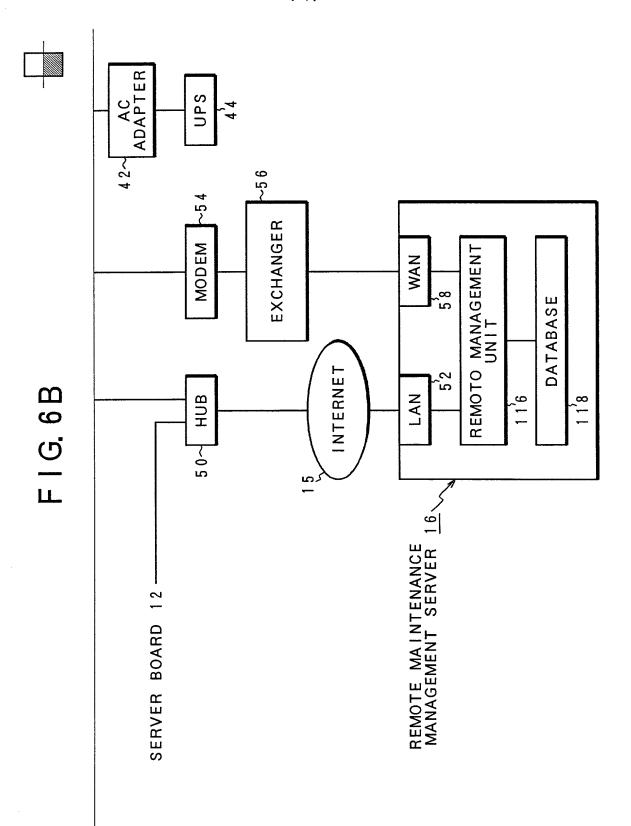
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TEMPERATURE SENSOR 34 INTEGRATED MANAGEMENT PANEL BOARD FAN MONITORING INTERFACE TEMPERATURE SENSOR FLASH ROM , 8 SERVER MACHINE F I G. 5A -84 - 182,1 1C2 CONTROLLER IMP CONTROLLER <12 80 82-2 1C2 CONTROLLER 84-2~ ф Φ POWER DISTRIBUTION UNIT 126 124 LED 1284 136-130-1327 347 SW 122-CD ر 2 8 12′0



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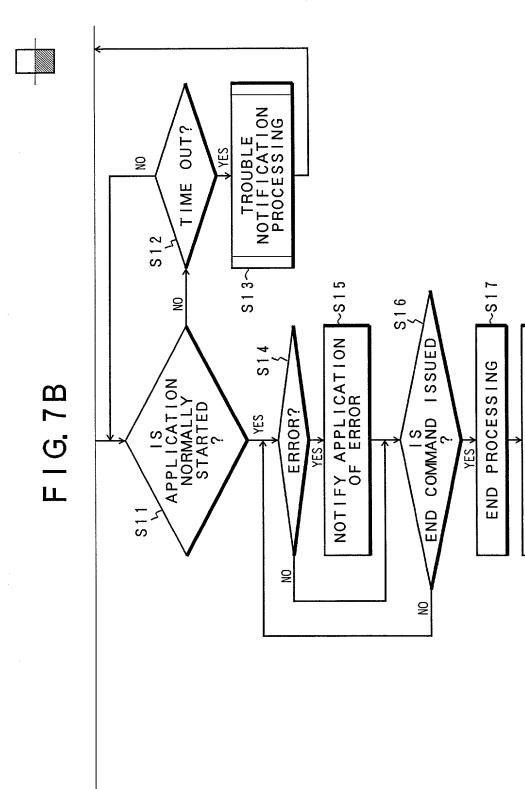
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POWER

OFF

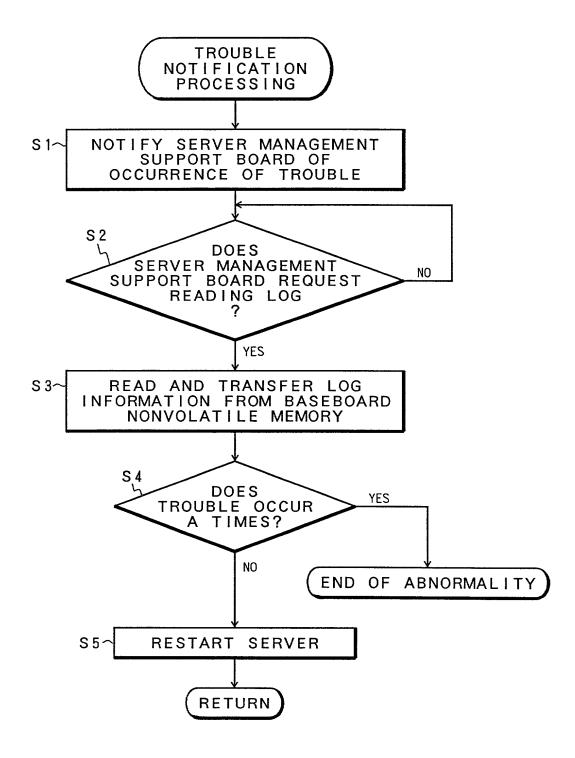
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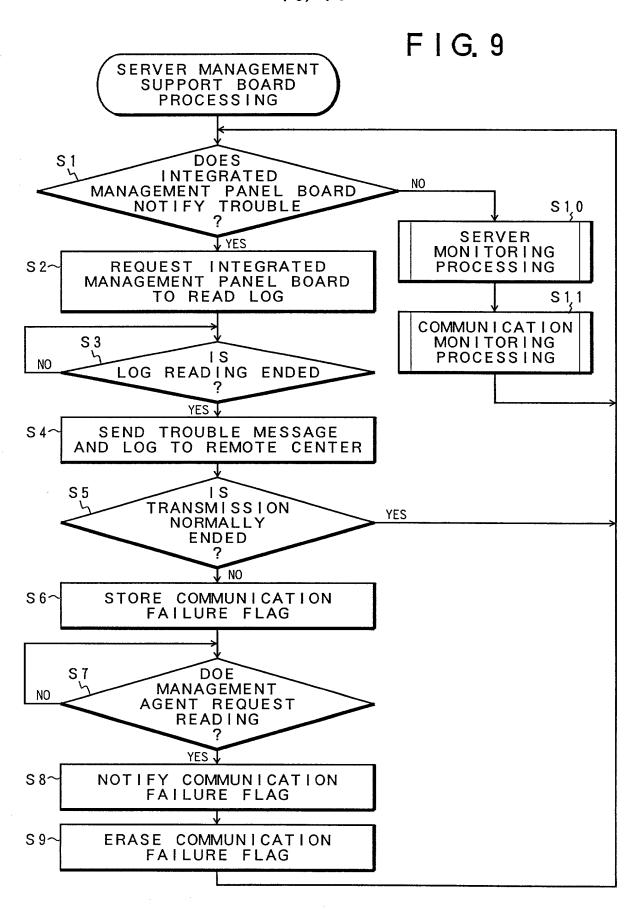
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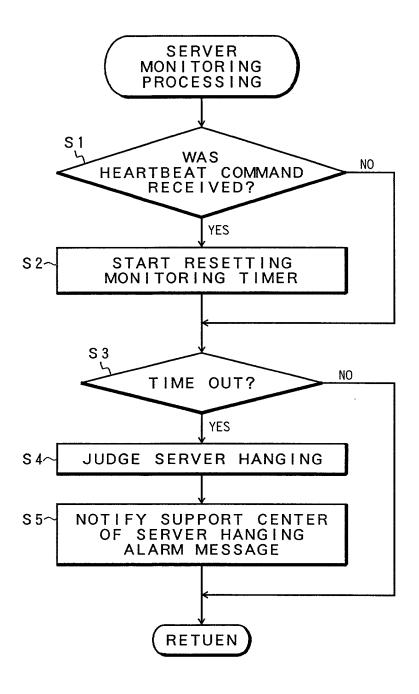
The Total Terry Terry Transport of the Terry Terry State (1992) and the Terry

F I G. 8

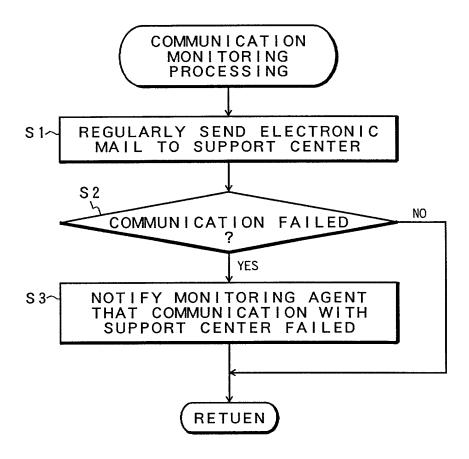




F I G. 10



F I G. 11



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F I G. 12

